ARCHANA TM

**Email**: archanamanjunath7@gmail.com  
Contact: (+91-9972750761)

**Objective:**

Seeking a position to utilize my skills and abilities in the Information Technology Industry that offers Professional growth while being resourceful, innovative and flexible to the organization.

**Education Qualification:**

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| --- | --- | --- | --- | --- |
| **Qualification** | **Year of passing** | **Institution** | **Board/University** | **Percentage** |
| B.E in Electronics & communication (till 6th semester) | 2018 | RRCE | VTU | 73.02% |
| Pre-university [PCMB] | 2014 | KPUC | PUE | 80.83% |
| SSLC | 2012 | NSM | KSEEB | 78.88% |

**Technical Skills:**

* **Programming languages:**

C, C++,Microcontroller, Microprocessor, HDL

* **Operating systems:**

Windows 7, 8.1, XP

**Areas of strength:**

* Confident and a good listener, self motivated with positive attitude
* Ability to deal with people diplomatically
* Team worker and flexible
* Enthusiastic to learn

**Achievements:**

* Won National level prize in “Sir Ramanujan Mathematical Competitions” conducted by Indian Society for Technical Education, New Delhi.
* Won 1st prize in Tech Rangoli at ANOKA-2K17(National Level Technical Fest).
* Won 1st prize in Quiz Competition conducted by “Centre for Strategy and International Affairs”.
* Won 2nd and 3rd price in many Technical and Non-Technical events.

**Projects:**

* Name:- "**A SMART BALLASTIC VEST**" project proposal for DST & Texas Instruments Inc. India Innovation Challenge Design Contest 2016.
* Description:- The person who wears this jacket offer complete protection against a wide range of threats ranging from fragments and shrapnel's to bullets.

**Extra-Curricular Activities:**

* Worked as a Co-ordinator of Technical Event called ELECTROBLITZ-1(National Level Symposium).
* Worked as a Co-ordinator of Technical Event called ELECTROBLITZ-2(National Level Symposium).
* Worked as a Co-ordinator of National Conference on “VLSI,COMMUNICATION AND SIGNAL PROCESSING”.
* Participated in 2days workshop on Design Thinking, innovation and implementation using “INTEL Edison Boards”, 2days workshop on Open Hardware projects, Proteus Simulation and PCB Design, Design Thinking ,Innovation And Implementation Using Intel Edison Boards, Touch Of PLC In Real World Applications, Technical Quiz and Circuit Debugging at CLONOELECTRIC (National level Symposium).
* Participated in “Personal Effectiveness Program” conducted by Indian Society for Technical Education, New Delhi.
* Participated in many Non-technical events and won prize in college and school level.

**Personal Details:**  
Name: ARCHANA TM  
Date of birth: 18 /02/1997

Father’s Name: Manjunath HB   
Languages known: English, Kannada.  
Nationality: Indian

**Declaration:**  
  
I hereby declare that the information furnished above is true to the best of my knowledge.

**Place:** Bangalore

**Archana TM**

**Date:**